How to Read a Datasheet

Prepared for the WIMS outreach program 5/6/02, D. Grover

In order to use a PIC microcontroller, a flip-flop, a photodetector, or practically any electronic device, you need to consult a datasheet. This is the document that the manufacturer provides telling you

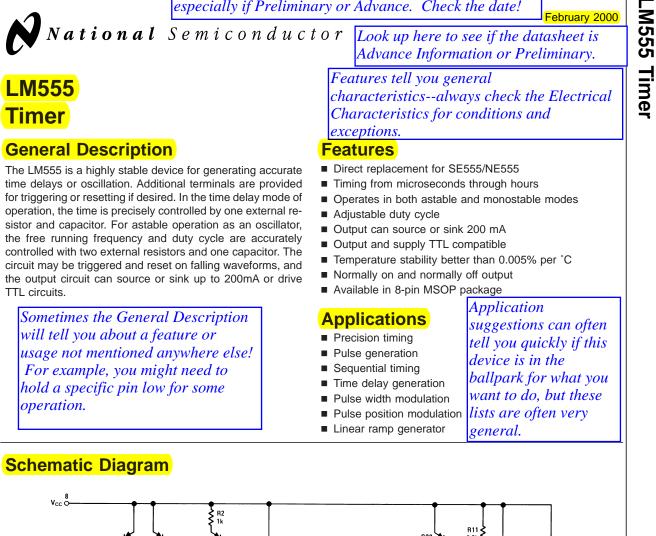
- the typical device performance
- minimum and maximum requirements and characteristics
- what you can do to the device without harming it
- suggested uses and hints

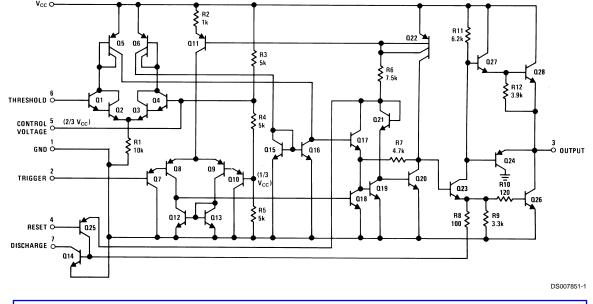
Manufacturers want you, the designer, to have a successful experience with their device. They are trying to be helpful. They don't always succeed. The datasheet on the following pages is a relatively good datasheet. It tries to concisely tell you everything you need to know about the device, a common 555 timer chip (the duct-tape of the electronics hobbyist). Most datasheets for ICs follow the same general layout.

You don't have to understand everything in a datasheet. There's a lot of information that might not be of any use to you. The annotations that follow try to point out parts of the datasheet that you should pay particular attention to.

Where do you find datasheets? Nowadays you can find almost any datasheet on the internet, often in PDF (Acrobat) form. For example, the LM555 datasheet from National Semiconductor is on their website at www.national.com.

What is the LM555? The LM555 is a timer chip that uses external resistors and capacitors to generate either a single pulse of a certain duration, or a continuous sequence of pulses with a variety of pulse widths possible. Because it is a very general purpose collection of functional blocks such as comparators, a flip-flop, internal voltage divider, high power output stage, and so on, a number of different timing-related functions are possible. Entire books have been written about the 555, though it is often used when another IC would work better. (See for example the CD4538 timer chip.) There will always be a date. Datasheets do change, especially if Preliminary or Advance. Check the date!



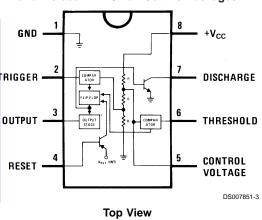


Usually called the Equivalent Schematic Diagram, this schematic isn't what is necessarily in the device, but the device acts as if this was what was inside. It can help explain behavior that isn't otherwise described in the datasheet. Could you duplicate this circuit on a breadboard? Only if you knew what the characteristics of the transistors were--which are not given.

Connection Diagram

Dual-In-Line, Small Outline and Molded Mini Small Outline Packages

Make sure you're looking at the pinout for the correct package. In the back pages you'll find drawings of the package types. Here all the packages have the same pinout--that's not always the case!



Ordering Information

Package	Part Number	Package Marking	Media Transport	NSC Drawing
8-Pin SOIC	LM555CM	LM555CM	Rails	M08A
	LM555CMX	LM555CM	2.5k Units Tape and Reel	MUOA
8-Pin MSOP	LM555CMM	Z55	1k Units Tape and Reel	MUA08A
	LM555CMMX	Z55	3.5k Units Tape and Reel	WIOA06A
8-Pin MDIP	LM555CN	LM555CN	Rails	N08E

Under Ordering Information you'll find a list of every variation of this device along with the COMPLETE part number. Often the first few letters are either industry-standard or identify the manufacturer (e.g., PIC). The generic identifier comes next ("555"). Suffixes generally give package type (surface mount and through hole types), temperature range (wider range = more expensive), speed (faster = more expensive), and other variations such as power, voltage range, etc.

Other elements in datasheets:

--Related devices, such as devices this supercedes, exactly replaces, or is replaced by

--Block diagrams of internals

--Information to support programming or configuring the device (registers, etc.) --Interfacing with other devices (including input/output characteristics)

LM555

LM555

	tell you what will damag	ge the chipNOT	the maximu	m operatir	ig innus	
Absolute Maximum	Ratings (Note 2)	2 for details.				
		Dual-In-Line Pac				
If Military/Aerospace specific please contact the National Se	Soldering (10 Seconds)			260		
Distributors for availability and specifications.		Small Outline Packages			201	
Supply Voltage	- +18V	(SOIC and MSOP)				
Power Dissipation (Note 3)	10	,	Vapor Phase (60 Seconds)			
LM555CM, LM555CN	1180 mW		Infrared (15 Seconds)			
LM555CMM	613 mW	See AN-450 "Surface Mounting Methods and				
Operating Temperature Ranges		on Product Reliability" for other methods of so surface mount devices.			Idering	
LM555C	0°C to +70°C				as solit	
Storage Temperature Range	−65°C to +150°C		Electrical Characteristics are sometim nto DC (power supply, static input/ou			
		characteristics)			-	
Electrical Characte	ristics (Notes 1, 2)			i iming, ine	se lell y	
$(T_A = 25^{\circ}C, V_{CC} = +5V \text{ to } +13)$	5V, unless othewise specified)	what you can co	uni on.			
Parameter	Conditions				Units	
	latch outthe datasheet n	0	LM555C			
di	iscuss more than one par	t! Min	Тур	Мах		
Supply Voltage		4.5		16	V	
Supply Current	$V_{CC} = 5V, R_{L} = \infty$		3	6		
	$V_{CC} = 15V, R_{L} = \infty$		10	15	mA	
	(Low State) (Note 4)					
Timing Error, Monostable Initial Accuracy Drift with Temperature	$R_{A} = 1k \text{ to } but \text{ not the}$	the minimum and his gives you an id worst-case. Goo	dea of the li	kely behav	ior, 🖕	
Initial Accuracy	Design to typical. Th	his gives you an id e worst-case. Goo	dea of the li	kely behav	ior, 🖞	
Initial Accuracy Drift with Temperature	$R_{A} = 1k \text{ to}$ $R_{A} = 0.4 \text{ J}$	his gives you an id e worst-case. Goo	dea of the li	kely behav	ior, 🖞	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply	$R_{A} = 1k \text{ to}$ $R_{A} = 0.4 \text{ J}$	his gives you an id e worst-case. Goo	dea of the li	kely behav	ior, 🖞	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply	$R_{A} = 1k \text{ to}$ $R_{A} = 0.4 \text{ J}$	his gives you an ia worst-case. Goo he typical!	dea of the li od, robust do	kely behav	ior, 🖞	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable	$R_{A} = 1k \text{ to}$ $C = 0.1\mu F$ $Design to$ $typical. The but not the count on th$	his gives you an id e worst-case. Goo he typical! conditions noted.	dea of the li od, robust do	kely behav	ior, not	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy	$R_{A} = 1k \text{ to}$ $C = 0.1 \mu F.$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O	dea of the li od, robust do	kely behav	ior, not %	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy	$R_{A} = 1k \text{ to}$ $C = 0.1\mu F$ $Design to typical. The but not the count on the count on the device is at a specific later on in the dataset.$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. Q heet will show	dea of the li od, robust do Here the ften, plots	kely behav esign does	ior, not %	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature	$R_{A} = 1k \text{ to} $ $C = 0.1 \mu F$ $Design to typical. The but not the count on the count on the count on the count on the count of th$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w	dea of the li od, robust do United the Here the ften, plots vell as those	kely behav esign does	ior, not % ppm/°	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply	$R_{A} = 1k \text{ to} $ $C = 0.1 \mu F,$ $Design to typical. The but not the count on the count on the count on the device is at a specific later on in the datase temperature-related$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w	dea of the li od, robust do United the Here the ften, plots vell as those	kely behav esign does	ior, not % ppm/° % %/V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage	$R_{A} = 1k \text{ to} $ $C = 0.1 \mu F,$ $Design to typical. The but not the count on the count on the count on the device is at a specific later on in the datase temperature-related$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w	dea of the li od, robust do United the Here the ften, plots vell as those	kely behav esign does	ior, not % ppm/° % %/V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage	$R_{A} = 1k \text{ to}$ $C = 0.1\mu\text{F}$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w	dea of the line od, robust do United the Here the ften, plots well as those tc.).	kely behav esign does	ior, not % ppm/° % %/V x V _{CC}	
Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature	$R_{A} = 1k \text{ to} $ $C = 0.1\mu F$ $Design to typical. The but not the count on the count on the count on the count on the device is at a specific later on in the datast temperature-related dependent on supply$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w	dea of the line od, robust do <u>United</u> Here the ften, plots rell as those tc.).	kely behav esign does	ior, not % ppm/° % %/V × V _{CC}	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage	$R_{A} = 1k \text{ to}$ $C = 0.1\mu\text{F}$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w	dea of the line od, robust do Here the ften, plots rell as those tc.). 5 1.67	kely behav esign does	ior, not % ppm/° % %/V × V _{cc} V V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage	$R_{A} = 1k \text{ to}$ $C = 0.1\mu\text{F}$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w voltage, speed, e	dea of the line od, robust do Here the ften, plots cell as those tc.). 5 1.67 0.5	kely behav esign does	ior, p not ν' % ppm/° %/V × V _{Ct} V V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage	$R_{A} = 1k \text{ to}$ $C = 0.1\mu\text{F}$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w voltage, speed, e	dea of the line od, robust de united for the file ften, plots cell as those tc.). 5 1.67 0.5 0.5	kely behav esign does	ior, b not ν' % ppm/° % %/V × V _{ct} V V V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage Trigger Current Reset Voltage Reset Current	$R_{A} = 1k \text{ to} C = 0.1 \mu \text{F}$ $Pay attention to the count on the dataset temperature-related dependent on supply$ $V_{CC} = 15V$ $V_{CC} = 5V$ (Note 6) $V_{CC} = 15V$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w voltage, speed, e 0.4 9	dea of the line od, robust de 0.1 0.1 0.5 0.1 0.1 10	kely behav esign does 0.9 1 0.4 0.25 11	ior, p not v % ppm/° % %/V × V _{Ct} V V V μΑ V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage Trigger Current Reset Voltage Reset Current Threshold Current Control Voltage Level	$R_{A} = 1k \text{ to} C = 0.1 \mu \text{F}$ $Pay attention to the count on the device is at a specific later on in the dataset temperature-related dependent on supply V_{CC} = 15V V_{CC} = 5V (Note 6)$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w voltage, speed, e 0.4	dea of the line od, robust de od, robust de Here the ften, plots vell as those tc.). 5 1.67 0.5 0.5 0.1 10 3.33	kely behav esign does 0.9 1 0.4 0.25 11 4	ior, h not ν ^o % ppm/ ^o % %/V × V _{cc} V V V μΑ V μΑ V	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage Trigger Current Reset Voltage Reset Current Threshold Current Control Voltage Level Pin 7 Leakage Output High	$R_{A} = 1k \text{ to} C = 0.1 \mu \text{F}$ $Pay attention to the count on the dataset temperature-related dependent on supply$ $V_{CC} = 15V$ $V_{CC} = 5V$ (Note 6) $V_{CC} = 15V$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w voltage, speed, e 0.4 9	dea of the line od, robust de 0.1 0.1 0.5 0.1 0.1 10	kely behav esign does 0.9 1 0.4 0.25 11	ior, b not V° % ppm/° % %/V × V _{cc} V V V μΑ V M A μΑ	
Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Timing Error, Astable Initial Accuracy Drift with Temperature Accuracy over Temperature Drift with Supply Threshold Voltage Trigger Voltage Trigger Current Reset Voltage Reset Current Threshold Current Control Voltage Level	$R_{A} = 1k \text{ to} C = 0.1 \mu \text{F}$ $Pay attention to the count on the dataset temperature-related dependent on supply$ $V_{CC} = 15V$ $V_{CC} = 5V$ (Note 6) $V_{CC} = 15V$	his gives you an id e worst-case. Goo he typical! conditions noted. c temperature. O heet will show parameters (as w voltage, speed, e 0.4 9	dea of the line od, robust de od, robust de Here the ften, plots vell as those tc.). 5 1.67 0.5 0.5 0.1 10 3.33	kely behav esign does 0.9 1 0.4 0.25 11 4	ior, h not ν ^o % ppm/ ^o % %/V × V _{cc} V V V μΑ V μΑ V	

LM555

Electrical Characteristics (Notes 1, 2) (Continued)

(T_A = 25°C, V_{CC} = +5V to +15V, unless othewise specified)

Parameter	Conditions	Limits LM555C			Units
		Min	Тур	Max	
Output Voltage Drop (Low)	$V_{\rm CC} = 15 V$				
	I _{SINK} = 10mA		0.1	0.25	V
	I _{SINK} = 50mA		0.4	0.75	V
	I _{SINK} = 100mA		2	2.5	V
	I _{SINK} = 200mA		2.5		V
	$V_{CC} = 5V$				
	I _{SINK} = 8mA				V
	I _{SINK} = 5mA		0.25	0.35	V
Output Voltage Drop (High)	I_{SOURCE} = 200mA, V_{CC} = 15V		12.5		V
	I_{SOURCE} = 100mA, V_{CC} = 15V	12.75	13.3		V
	$V_{CC} = 5V$	2.75	3.3		V
Rise Time of Output			100		ns
Fall Time of Output			100		ns

Note 1: All voltages are measured with respect to the ground pin, unless otherwise specified.

Note 2: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.

Note 3: For operating at elevated temperatures the device must be derated above 25°C based on a +150°C maximum junction temperature and a thermal resistance of 106°C/W (DIP), 170°C/W (S0-8), and 204°C/W (MSOP) junction to ambient.

Note 4: Supply current when output high typically 1 mA less at $V_{CC} = 5V$.

Note 5: Tested at V_{CC} = 5V and V_{CC} = 15V.

Note 6: This will determine the maximum value of $R_A + R_B$ for 15V operation. The maximum total ($R_A + R_B$) is 20M Ω .

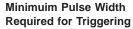
Note 7: No protection against excessive pin 7 current is necessary providing the package dissipation rating will not be exceeded.

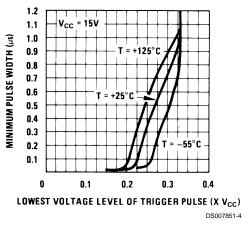
Note 8: Refer to RETS555X drawing of military LM555H and LM555J versions for specifications.

(*Here is Note 2 in large print*)

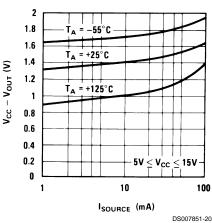
Note 2: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.

Typical Performance Characteristics

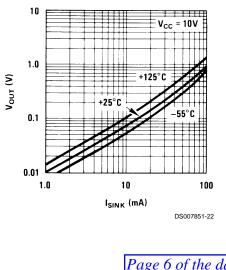




High Output Voltage vs. Output Source Current

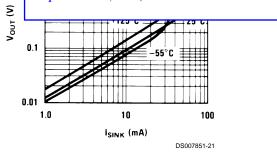


Low Output Voltage vs. Output Sink Current

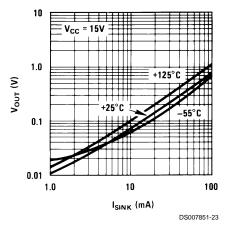


Supply Current vs. Supply Voltage 12 10 -55[°]C SUPPLY CURRENT (mA) 8 +25°C 6 +125°C 4 2 0 10 15 5 SUPPLY VOLTAGE (V) DS007851-19

Low Output Output Sink Graphs are used to describe characteristics that can't be captured easily in a table. Often Output Sink several things are being varied--above, supply current is measured as voltage is changed, but this is also being show for three different temperatures. Note that 25C is roughly room temperature (77F).



Low Output Voltage vs. Output Sink Current



Page 6 of the datasheet is omitted.

Here are example circuits and application notes. Note too that often there are other sources for application information, such as separate Application Notes available from the manufacturer.

These waveforms would be

helpful in

debugging a circuit!

Applications Information

MONOSTABLE OPERATION

In this mode of operation, the timer functions as a one-shot (*Figure 1*). The external capacitor is initially held discharged by a transistor inside the timer. Upon application of a negative trigger pulse of less than $1/3 V_{CC}$ to pin 2, the flip-flop is set which both releases the short circuit across the capacitor and drives the output high.

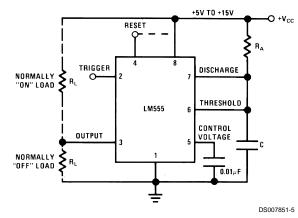
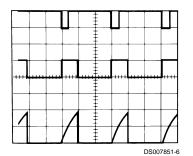


FIGURE 1. Monostable

The voltage across the capacitor then increases exponentially for a period of t = 1.1 R_A C, at the end of which time the voltage equals 2/3 V_{CC}. The comparator then resets the flip-flop which in turn discharges the capacitor and drives the output to its low state. *Figure 2* shows the waveforms generated in this mode of operation. Since the charge and the threshold level of the comparator are both directly proportional to supply voltage, the timing internal is independent of supply.



 $\begin{array}{ll} V_{CC} = 5V & \mbox{Top Trace: Input 5V/Div.} \\ TIME = 0.1 \mbox{ ms/DIV.} & \mbox{Middle Trace: Output 5V/Div.} \\ R_A = 9.1 \mbox{k}\Omega & \mbox{Bottom Trace: Capacitor Voltage 2V/Div.} \\ C = 0.01 \mbox{μF} \end{array}$

FIGURE 2. Monostable Waveforms

During the timing cycle when the output is high, the further application of a trigger pulse will not effect the circuit so long as the trigger input is returned high at least 10µs before the end of the timing interval. However the circuit can be reset during this time by the application of a negative pulse to the reset terminal (pin 4). The output will then remain in the low state until a trigger pulse is again applied.

When the reset function is not in use, it is recommended that it be connected to $V_{\rm CC}$ to avoid any possibility of false triggering.

Figure 3 is a nomograph for easy determination of R, C values for various time delays.

NOTE: In monostable operation, the trigger should be driven high before the end of timing cycle.

LM555

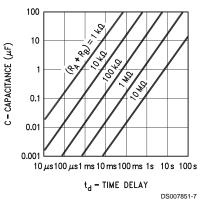


FIGURE 3. Time Delay

ASTABLE OPERATION

If the circuit is connected as shown in *Figure 4* (pins 2 and 6 connected) it will trigger itself and free run as a multivibrator. The external capacitor charges through $R_A + R_B$ and discharges through R_B . Thus the duty cycle may be precisely set by the ratio of these two resistors.

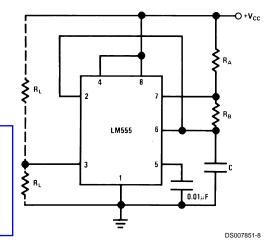
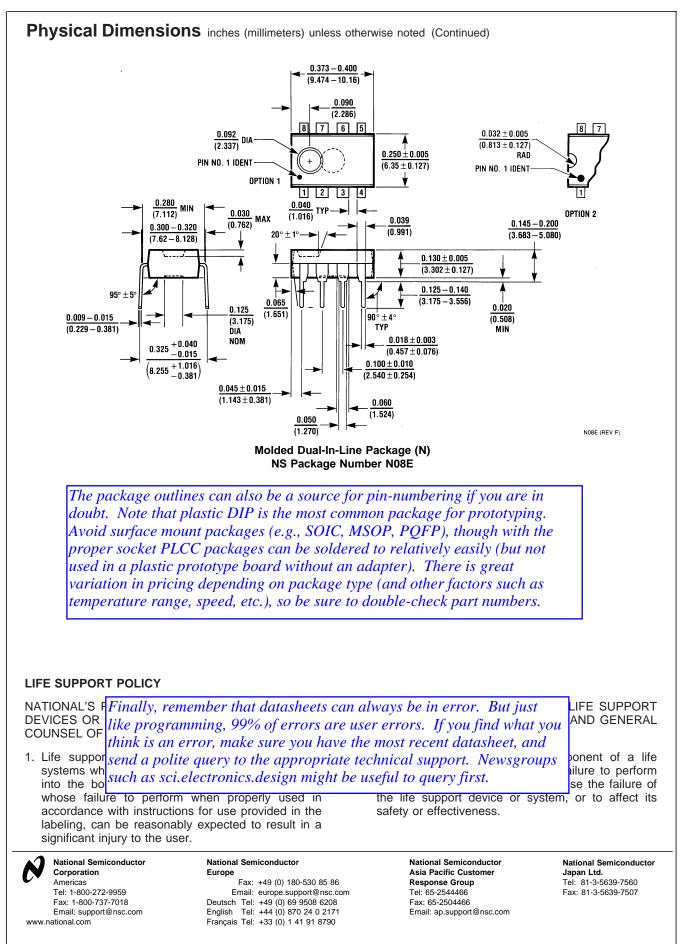


FIGURE 4. Astable

In this mode of operation, the capacitor charges and discharges between 1/3 V_{CC} and 2/3 V_{CC} . As in the triggered mode, the charge and discharge times, and therefore the frequency are independent of the supply voltage.

Not all datasheet application examples are so well written--sometimes you just get the raw schematics. For more complex devices, such as microcontrollers, different aspects might be handled in different sections--for example, a clock circuit in one part, a reset circuit in another. Read over all the sections to make sure you are using the device correctly and have supplied all the necessary components. LM555 Timer



National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.